

**AMENDMENTS TO THE SPECIFICATION**

Please replace paragraph [0033] with the following paragraph:

[0033] The host processor sends an indication to a master processor 430 in the master row engine 410, indicating the type of video data that is to be decoded. Responsive to receiving the signal, the master processor 430 loads the appropriate combination of instructions 295a, 295b, 295c, 295d from the instruction memory into a code data memory 425. ~~[[[or is it the host processor that loads the instructions?? ]]]~~ If the indicator indicates that the video data is MPEG-2, the master processor 430 also loads the appropriate combination of instructions 295a, 295b, 295c, 295d from the instruction memory into a code data memory 550 in the slave row engine 410. After loading the appropriate combination of instructions 295a, 295b, 295c, 295d, the appropriate combination of instructions cause the master row engine 405 to access the video data with a video DMA 420.